

Donald Kymitszke

Objective A leadership hardware development position in a dynamic and fast growing organization which could fully utilize my drive and diverse background.

Strengths Personal hands-on experience:

- Management of Engineering groups, budgets, schedules, and proposals.
- Ability to lead large diverse teams to common goal.
- Guidance, direction, and motivation for Engineering design teams.
- Technical lead of “big-picture” for design, test, and production.
- Conceptualization of creative and unique solutions to difficult problems.
- Generation of product definitions, specifications, engineering documentation, test plans, and reports.
- Transition of Engineering designs to Production (DFM, DFT).
- Mentor of college graduates.
- Implementation of procedures to optimize design, prototyping, and production (ISO-9001).
- Experience in Electrical, Mechanical, Electronic Packaging, and Thermal designs.
- Versed in digital and analog circuit design including PCB layout design.
- Literate in Excel, Word, Project, PowerPoint, Visio, ORCAD, Concept HDL, AutoCAD, and others.

Experience 1997 – 2002 Force Computers Fremont, CA
Hardware Development Manager

- Managed Hardware Engineering group developing open architecture CompactPCI computer systems.
- Repeatedly delivered architectures, product definitions, specifications, and detail designs of High Available Central Office NEBS Level 3 based systems, meeting tight schedules.
- Established common design continuity across all programs, provided core technical interface with customers, and instituted thorough design review processes.
- Expedited development of critical PICMG 2.16 – Compliant Packet Based Ethernet Switching Platforms, giving company six-month marketing edge.
- Led design team in Telcom Alarm Module development, providing environmental processor functions, Hot Swap controller, IPMI, and SNMP, within redundant system architecture.
- Developed award winning “best of show” HA ultra SPARC-III processor based cPCI cluster server.
- Major contributor developing key OEM scalable servers requiring: Intel SBC, hot swappable redundant power supplies and cooling, RAID PMC board, SCA media bay, cPCI backplane, and Telcom Alarm Module.
- Managed vendors in developing variety of sub systems.

1995 – 1997 Seagate Technology Scotts Valley, CA
Senior Development Engineer

- Member of core team developing mobile disc drives, ensuring all aspects of the designs met or exceeded product requirements.
- Directed “design for reliability” using Failure Mode and Effects Analysis.
- Performed MTBF predictive modeling, setting component reliability levels necessary to achieve specified objectives.

1993–1995 Eagle Technology San Jose, CA
Senior Hardware Design Engineer

- Led all board-level designs of Novell NIC adapters for PCI, PCMCIA, EISA, and ISA buses in high-volume manufacturing environment.
- Principal designer of “Plug ‘n Play” Ethernet adapters, working closely with Intel to design-in latest controllers that resulted in launch of new product family with competitive edge.
- Led aggressive NE2000 PCB designs, resolving FCC and grounding issues to provide cost-reduction.

1992–1993 New Mark Systems, Inc. Fremont, CA

Engineering Manager

- Managed multi-functional group developing and manufacturing Intel-based rugged computer systems with strong emphasis on quality and responsiveness to customers.
- Retained major account, despite outside competition, through extensive customer contact and improving quality by upgrading final test in production.

1991–1992 Chaparral Communications San Jose, CA

Project Manager

- Technically directed concurrent-engineering team in complex, fast-paced, high-volume environment, introducing new methods into product development that cut design cycle time.
- Developed satellite receiver architecture, reducing a three-processor design to one, then led low cost circuit and SMT PCB designs, ensuring analog circuitry noise margins.

1990–1991 QUME Corporation Milpitas, CA

Hardware Manager

- Managed Hardware Engineering group developing RISC-based printer controllers.
- Developed architecture of high-resolution printer controllers with Ethernet and SCSI interfaces, then led analog, digital, ASIC, and SMT PCB designs, along with OEM technology transfers.

1987–1990 ARGO Systems, Inc. Sunnyvale, CA

Senior Technical Staff

- Member of team designing 68K based VME embedded systems using multiple DSP32 CPUs, resulting in establishment of core digital signal processing products.
- Managed IR&D projects, system requirements, tracked cost, and schedules, ensuring systems were within budget and properly designed.

1983–1987 ADAC Laboratories Milpitas, CA

Senior Electronics Engineer

- Led hardware design team developing high resolution medical imaging workstations.
- Developed many key video, SCSI, data acquisition, and interface subsystems involving high-speed digital and analog circuit designs, increasing image processing systems' competitiveness.

1978–1983 Woodward-Clyde Consultants San Francisco, CA

Hardware Manager and Senior Electronics Engineer

- Managed technical staff developing geophysical systems using latest microprocessors, storage devices, data acquisition, RF telemetry, and low power low noise analog designs.
- Developed company's first real-time systems increasing usable data from 40% to 80%.

1976–1978 Geonomics, Inc. Berkeley, CA

Electronics Engineer

- Designed and produced geophysical systems, most notably introducing system calibration hardware and methodologies to provide quality data collection.

Education and Professional Training

1997–1999 Cadence Course Certificates in; Concept HDL, Concept Front to Back, ORCAD, and High Speed Design Using SPECCTRAQuest

1996 Kepner Tregoe Course Certificate in; Problem Solving and Decision Making

1975 Bachelors Electrical Engineering Technology
Rochester Institute of Technology Rochester, NY

Associations

IEEE Communications and Computer Societies